Docket No.: 43876-144 **PATENT** 

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Customer Number: 20277

**Taig HANSEN, et al.** 

Confirmation Number: 4587

Serial No.: 10/616,303

Group Art Unit: 2183

Filed: July 10, 2003

Examiner: Unknown

For:

PROGRAMMABLE PROCESSOR AND METHOD WITH WIDE OPERATIONS

### SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

To prevent excessive duplication, only copies of references that are not already of record in a parent are being submitted herewith. The examiner is invited to contact the undersigned if any other copies are needed.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

Lawrence T. Cullen Registration No. 44,489

600 13<sup>th</sup> Street, N.W. Washington, DC 20005-3096 (202) 756-8000 LTC:tlb

Facsimile: (202) 756-8087

Date: December 19, 2003

# INFORMATION DISCLOSURE CITATION IN AN **APPLICATION**

(PTO-1449)

ATTY. DOCKET NO. 43876-144

SERIAL NO. 10/616,303

**APPLICANT** 

Craig HANSEN, et al.

FILING DATE July 10, 2003 **GROUP** 2183

#### **U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS		Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applic Document		, Columns, Lines			
INITIALS	NO.			MIM-UU-TTTT	Document	Fass	ages of Relevan	it rigures App		
·		US	4,785,393	11/15/1988	Chu et al.					
	†	US 4,814,976		03/21/1989	Craig C. Hansen, e	t al.				
		US 5,031,135		07/09/1991	Patel					
		US 5,280,598 US 5,481,686 US 5,487,024 US 5,600,814 US 5,740,093 US 5,742,840		- 01/1994	Osaki et al.					
				01/02/1996	Dockser					
				01/1996	Girardeau Jr.					
				02/1997	Gahan et al.					
-				04/14/1998	Sharangpani					
				04/21/1998	Hansen et al.					
		US	5,768,546	06/1998	Kwon					
		US	5,898,849	04/27/1999	Tran					
		us	5,996,057	11/30/1999	Hunter L. Scales, III,	et al.				
		US 6,041,404 US 6,052,769		03/21/2000	Patrice Roussel, etla. Thomas R. Huff, et al					
				04/18/2000						
		US	6,173,393 B1	01/09/2001	Salvador Palanca, e	et al.				
		US	6,275,834 B1	08/14/2001	Derrick Chu Lin, et	t al				
		US 6,295,599		09/2001	Hansen et al.					
				FOREIGN PAT	ENT DOCUMENTS					
XAMINER'S		For	eign Patent Document	Publication Date	Name of Patentee or Applicant	Pages, Columns, Lines		anslation		
INITIALS	CITE NO.	Country Codes -Number 4 -Kind Codes (if known)		MM-DD-YYYY	of Cited Document	Where Relevant Figures Appear Yes				
	NQ.				,			No		
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XAMINER'S	T .	Include					agazine igurnal	···		
INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.								
		IEEE Draft Standard for "Scalable Coherent Interface-Low-Voltage Differential Signal Specifications And Packet Encoding", IEEE Standards Department, P1596.3/D0.15 (March 1992)								
		IEEE Draft Standard for "High-Bandwidth Memory Interface Based on SCI Signaling Technology (RamLink)", IEEE Standards Department, Draft 1.25 IEEE P1596.4-199X (May 1995)								
		IBM, "The PowerPC Architecture: A Specification For A New Family of Risc Processors", 2nd Ed., Morgan Kaufmann Publishers, Inc., (1994).								
		Hewlett-Packard Co., "PA-RISC 1.1 Architecture and Instruction Set", Manual Part No. 09740-90039, (1990).								
		ļ								
					PS R4000 User's Manual", Mfg. Pa					

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

**PATENT** PATENT

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## 10/616,303

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CITATION IN AN APPLICATION					ATTY. DOCKET NO. 43876-144 SERIAL NO. 10/616,303					
			APPLICANT HANSEN, et al.							
PRADEMAN.	(PTO-	1449)		FILING DATE July 10, 2003	GROUP <b>2183</b>					
		Ţ	J.S. PATEN	T	DOCUMENTS					
EXAMINER'S INITIALS	PATENT NO.	1		AME	CLASS	SUBCLASS	FII	LING DATE		
	4,025,772	05/24/77	Constant		· · · · · · · · · · · · · · · · · · ·					
	4,489393	12/18/84	Kawahara,	et	al.					
	4,701,875	10/20/87	Konishi, et	al	•				- · · ·	
	4,727,505	02/23/88	Konishi, et	al	•					
	4,876,660	10/24/89	Owens, et a	al.						
	4,893,267	01/09/90	Alsup, et al	1.						
	4,956,801	09/11/90	Priem et al							
	Montoye, et al.									
4,975,868 12/04/90 Freerk							·			
	5,032,865 07/16/91 Schlunt									
	5,157,388	10/20/92	Kohn							
	5,201,056	04/06/93	Daniel, et a							
	5,268,855 12/07/93 Mason, et a			al.						
	5,268,995	12/07/93	Diefendorf	f,	et al.					
							<u> </u>			
		FOR	REIGN PAT	E	NT DOCUMEN	TS <sub>E</sub>		às.		
EXAMINER'S INITIALS	PATENT NO.	DATE		CO	UNTRY	CLASS	SUBCLASS	Yes	Translation No	
	OTHER	ART (Incl	uding Autho	or,	Title, Date, Per	tinent P	ages, Etc.)			
	Parallel Comp Jim Lawson, F				tions, Adam Lev ı, 1987	inthal, P	at Hanrahan,	, Mike	Paquette,	
	Organization of the Motorola 88110 Superscalar RISC Microprocessor, Keith Diefendorff and Michael Allen.									
	1 1	1 /			er 13, October 4, CPU in MCM A		_		mance	
	IBM Creates I with Extension				5/400, Two New y 31, 1995.	CPU's Ir	nplement 64	-Bit Po	ower PC	
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EXAMINER'S	.· <u>.</u>		.s. I AILIN	1 DOCUME!	115		FII	ING DATE		
INITIALS				NAME	CLASS	SUBCLASS				
	5,408,581	04/18/95	Suzuki, et							
	5,423,051	06/06/95	Fuller, et a	l						
	5,426,600	06/20/95	Nakagawa,	et al.						
	5,500,811	03/19/96	Corry							
	5,557,724	09/17/96	Sampat, et	al.						
	5,588,152	8,152 12/24/96 Dapp, et al.								
	5,592,405	01/07/97	Gove, et al	•						
	5,640,543	06/17/97	Farrell, et a	ıl.						
	5,642,306	06/24/97	Mennemei	er, et al.						
	5,666,298	09/09/97	Peleg, et al.							
	5,669,010	09/16/97	Duluk, Jr.							
	5,673,407	09/30/97	Poland, et	al.						
	5,675,526	10/07/97	Peleg, et al.							
	5,680,338	10/21/97	Agarwal, e	t al.						
		FOR	EIGN PAT	ENT DOCUM	MENTS					
EXAMINER'S INITIALS	PATENT NO.	COUNTRY	OUNTRY CLASS		Translation					
		DATE				SUBCLASS	Yes	No		
,	0 474 246 A2		Japan			ļ				
~.	0 654 733 A1	L	Germany			24.05 \$		* ** *		
				or, Title, Date	V / //////////////////////////////	and the second second				
	The Visual Ins Prabhu, G. Zyı		•	raSPARтм, L.	. Kohn, G. M	aturana, M. '	[remb	lay, A.		
	Osborne McGraw-Hill, i860тм Microprocessor Architecture, Neal Margulis, Foreword by Les Kohn									
	A General-Purpose Array Processor for Seismic Processing, Nov-Dec., 1984, Volume 1, No. 3) Revisiting past digital signal processor technology, Don Shaver- Jan-Mar. 1998.									
	Accelerating Multimedia with Enhanced Microprocessors, Ruby B. Lee, 1995.									
EXAMINER				DATE CONS	DATE CONSIDERED					

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		Ţ	J.S. PATENT	DOCUMENTS					
EXAMINER'S INITIALS	PATENT NO.	DATE	N	NAME		SUBCLASS	FIL	ING DATE	
_	5,721,892	02/24/98	Peleg, et al.						
	5,734,874	03/31/98	Van Hook, et	al.					
	5,757,432	05/26/98	Dulong, et al	•					
	5,758,176 05/26/98 Agar			ıl.					
	5,802,336	09/01/98	Peleg, et al.						
	5,809,292	Wilkinson, e	t al.						
	5,818,739								
	5,825,677	10/20/98	Agarwal, et a	ıl.					
	5,835,782	11/10/98	Chu Lin, et a	1.			_		
	5,886,732	03/23/99	Humpleman						
	5,922,066	07/13/99	Cho, et al.						
	5,983,257	11/09/99	Dulong, et al	•					
	6,016,538	01/18/00	Guttag, et al.						
-	6,092,094	07/18/00	Ireton						
	6,401,194 B1	06/04/02	Nguyen, et a	<b>l.</b>					
		FOR	EIGN PATE	NT DOCUMEN	NTS			* ************************************	
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